**PATENT** 

I hereby certify that on the date specified below, this correspondence is being deposited with the United States Postal Service as first-class mail in an envelope addressed to the Commissioner for Patents, P.O. Box 1450,

Alexandria, VA 22313-1450.

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Ayesha J. Shaikh

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Joseph M. Jeddeloh

Attorney Docket No.: 501304.01

Serial No. : 10/625,132

Group Art Unit : Not Yet Assigned

Filed : July 22, 2003

Examiner

: Not Yet Assigned

Title

: APPARATUS AND METHOD FOR DIRECT MEMORY ACCESS IN A HUB-BASED

**MEMORY SYSTEM** 

## SUPPLEMENTAL INFORMATION DISCLOSURE STATEMENT

Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

Sir:

In accordance with 37 C.F.R. §§ 1.56 and 1.97 through 1.98, applicant wishes to make known to the Patent and Trademark Office the references set forth on the attached form PTO-1449. A copy of the cited non-patent literature reference, as required under 37 C.F.R. § 1.98(a)(2), is enclosed. Copies of the cited U.S. patents and U.S. patent application publications will not be submitted herewith in accordance with the waiver by the Office of the requirement under 37 C.F.R. § 1.98(a)(2)(i) for U.S. national patent applications filed after June 30, 2003. Although the aforesaid references are made known to the Patent and Trademark Office in compliance with applicant's duty to disclose all information he is aware of which is believed relevant to the examination of the above-identified application, applicant believes that his invention is patentable.

EFS-Web Receipt date: 02/27/2004 10625132 - GAU: 2182

Applicants also wish to make known to the Patent and Trademark Office that an application, Serial No. 10/645,288, describing and claiming subject matter that is similar to the subject matter of this application, was filed on August 20, 2003. This related application, as well as any prior art cited therein, may be material to the examination of this application. A copy of this related application as filed is submitted herewith in accordance with 37 C.F.R. § 1.98(d).

I hereby certify that no item set forth on the attached form PTO-1449 was cited in a communication from a foreign patent office in a counterpart foreign application or, to my knowledge, after making reasonable inquiry, was known to any individual designated in 37 C.F.R. § 1.56(c) more than three months prior to the filing of this Supplemental Information Disclosure Statement.

Please acknowledge receipt of this Supplemental Information Disclosure Statement and kindly make the cited references of record in the above-identified application.

Respectfully submitted,

DORSEY & WHITNEY LLP

Kimton N. Eng Registration No. 43,605

KNE:ajs

Enclosures:

Postcard Form PTO-1449 Cited Reference (1) Copy of Related Application (1)

1420 Fifth Avenue, Suite 3400 Seattle, WA 98101 Telephone (206) 903-8800 Facsimile (206) 903-8820

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FORM PTO-1449 (REV.7-80)

FEB 2 7 700 S U.S. DEPARTMENT OF COMMERCE PATENT AND TRADEMARK OFFICE

INFORMATION DESCLOSURE STATEMENT

(Oscillatory)

U.S. DEPARTMENT OF COMMERCE PATENT AND TRADEMARK OFFICE

501304.01

APPLICATION NO. 10/625,132

APPLICANT(S)

Joseph M. Jeddeloh

Filing Date

GROUP ART UNIT

INFORNATION DESCLOSURE STATEMENT (Oscillatorial sheets if necessary)					Joseph M. Jeddelo FILING DATE July 22, 2003	GRO	OUP ART UNIT of Yet Assis	ned	
<u>'</u>				PATENT I	DOCUMENTS				
*EXAMINER INITIAL		DOCUMENT NUMBER DATE			NAME CLASS		SUBCLASS	FILING IF APPR	G DATE
	AA	5,818,844	10/06/98	Singh et a	1.	370	463		
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	AJ	2003/0229770 A1	12/11/03	Jeddeloh		711	213		
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	AQ	Intel, "Intel 840 pp. 1-178.	Chipset: 82	840 Memor	ry Controller Hub (1	MCH)", Data	sheet, Octo	ber 19	199,
	AR								
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